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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

TAE-SUNG KIM

Serial No.: 10/766,564

Examiner: ERDEM, FAZLI

Filed: 29 January 2004

Art Unit: 2826

For: NOVEL CONDUCTIVE ELEMENTS FOR THIN FILM TRANSISTORS USED  
IN A FLAT PANEL DISPLAY

### INFORMATION DISCLOSURE STATEMENT

Paper No. 12

Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.56, and §§1.97 and 1.98 as amended, Applicant cites, describes and provide copies of the following art reference.

- Japanese Patent Publication No. 7-58110 to Yamaha *et al.*, entitled *SEMICONDUCTOR DEVICE*, published on 3 March 1995.

Yamaha *et al.* '110 aims at prevention of generation of alloy pits at substrate connections and generation of Al hillocks at interlayer connections in a multilayer interconnection structure of a semiconductor device. An English language Abstract is attached.

It should be noted that Yamaha *et al.* '110 was cited in an Office action issued by the Korean Intellectual Property Office on 25 July 2005 in the corresponding Korean Patent Application No. 10-2003-0063583. A copy of the Office action and its English language translation are also attached.

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It is submitted that each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. Accordingly, there is no fee is incurred by filing of this Information Disclosure Statement.

The citation of the foregoing references is not intended to constitute an assertion that other or more relevant art does not exist. Accordingly, the Examiner is requested to make a wide-ranging and thorough search of the relevant art.

Respectfully submitted,



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Folio: P57001  
Date: 9/29/05  
I.D.: REB/kf

